ABSTRACT

In order to provide a multi-layer electronic component in which the occurrence of delamination between the ceramic layer and the internal electrode is restricted and a method for manufacturing the same, the multi-layer electronic component of the present invention comprises a stack formed by stacking piezoelectric layers and internal electrodes one on another alternately and a pair of external electrodes formed on two opposing side faces of the stack, wherein the internal electrode consists of a first internal electrode connected to the external electrode formed on one of the two side faces and a second internal electrode located between the first internal electrode and connected to the external electrode formed on the other one of the two side faces, and wherein the internal electrodes and the piezoelectric layers are faced in proximity so that a space between them is 2 μm or less over an area occupying 50% or more of the active region where the first internal electrode and the second internal electrode oppose each other.